



APLUS.0017A

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Songjie Xu
Appl. No. : 09/754,406
Filed : January 2, 2001
For : METHODOLOGY AND
APPLICATIONS OF TIMING-
DRIVEN LOGIC RESYNTHESIS
FOR VLSI CIRCUITS
Examiner : Thomas H. Stevens
Group Art Unit : 2123

CERTIFICATE OF MAILING

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

August 9, 2005

(Date)

Raimond J. Salenieks, Reg. No. 37,924

RESPONSE TO SECOND OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated March 9, 2005 in the above-referenced patent application, please make the following amendments:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.